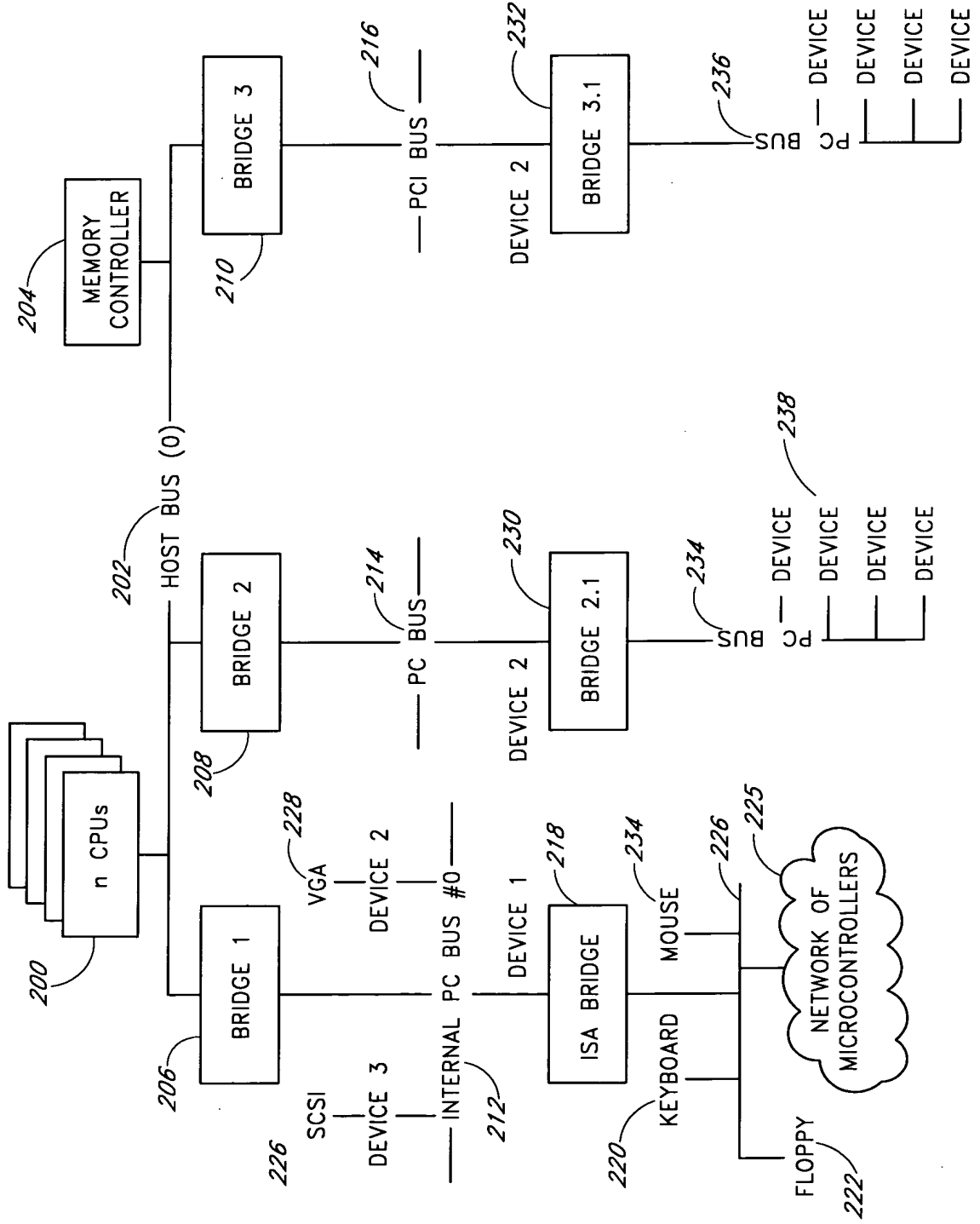
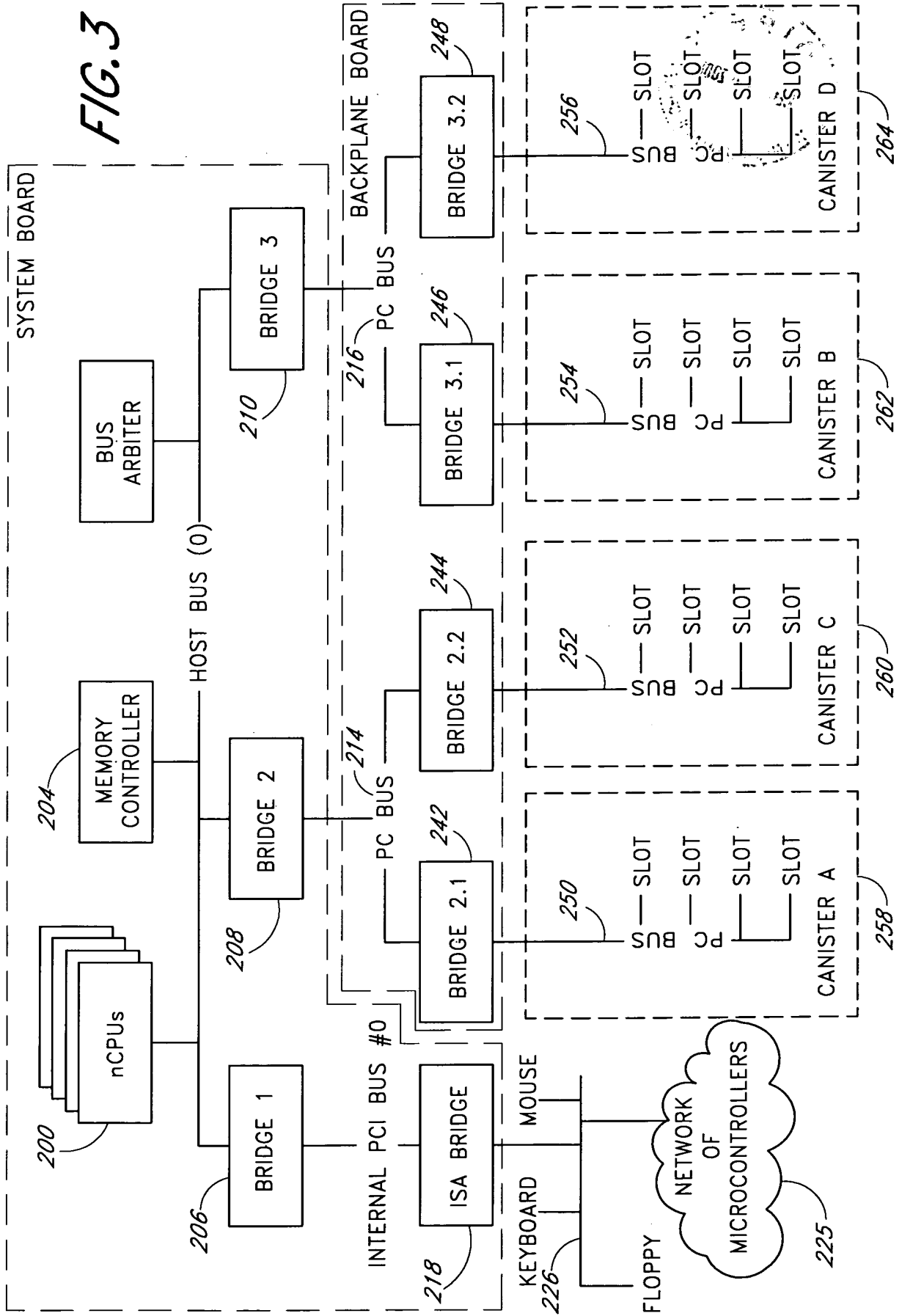


FIG. 1

FIG. 2





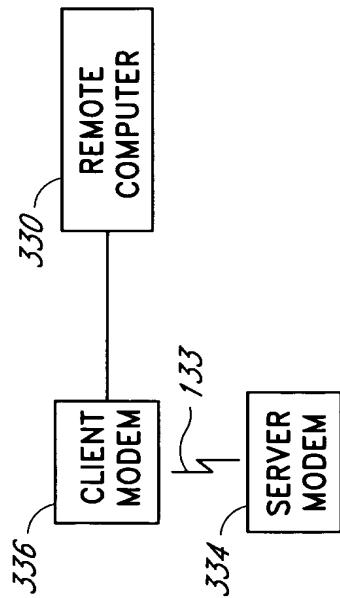
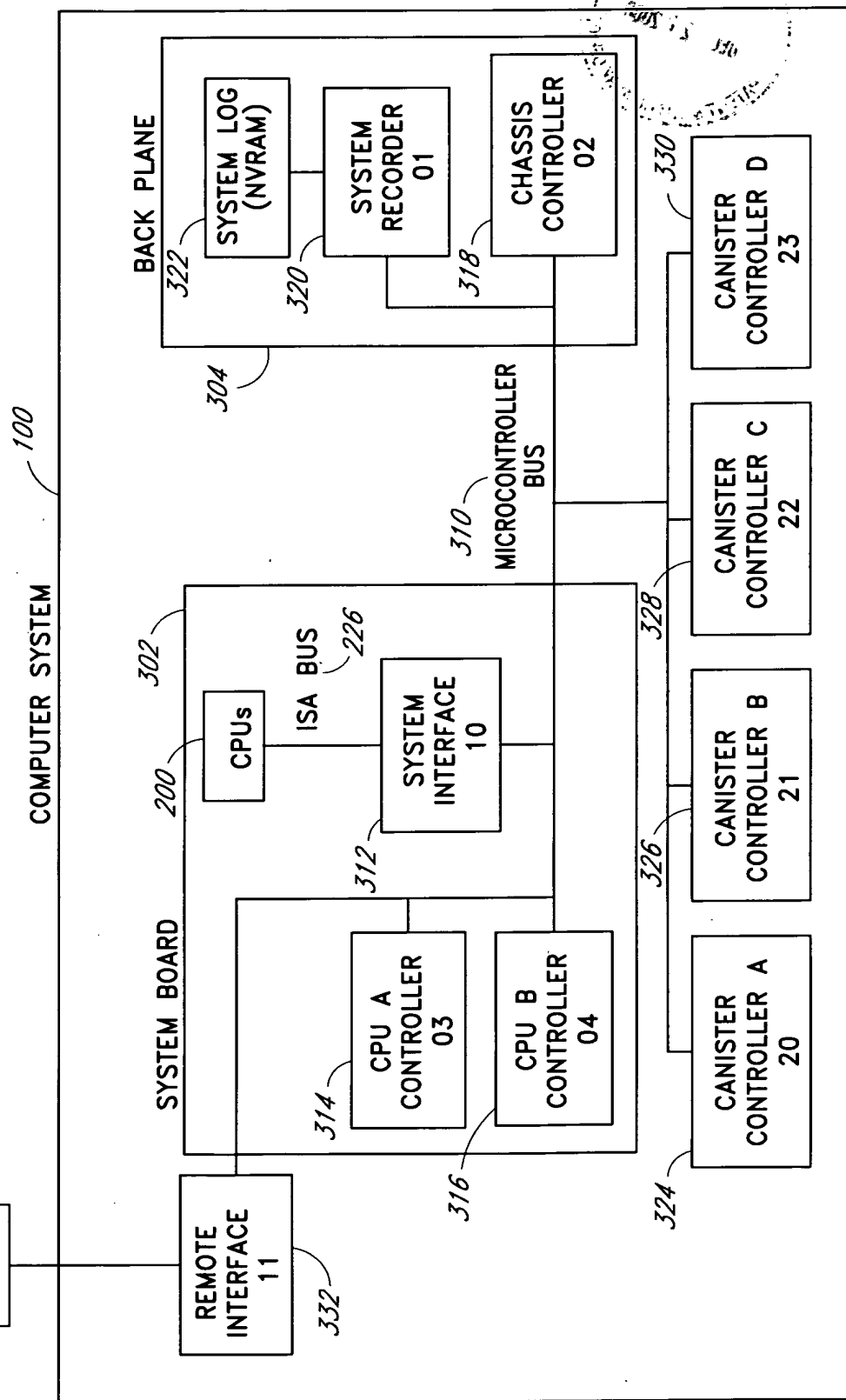


FIG. 4



Microcontroller Network Bus

A

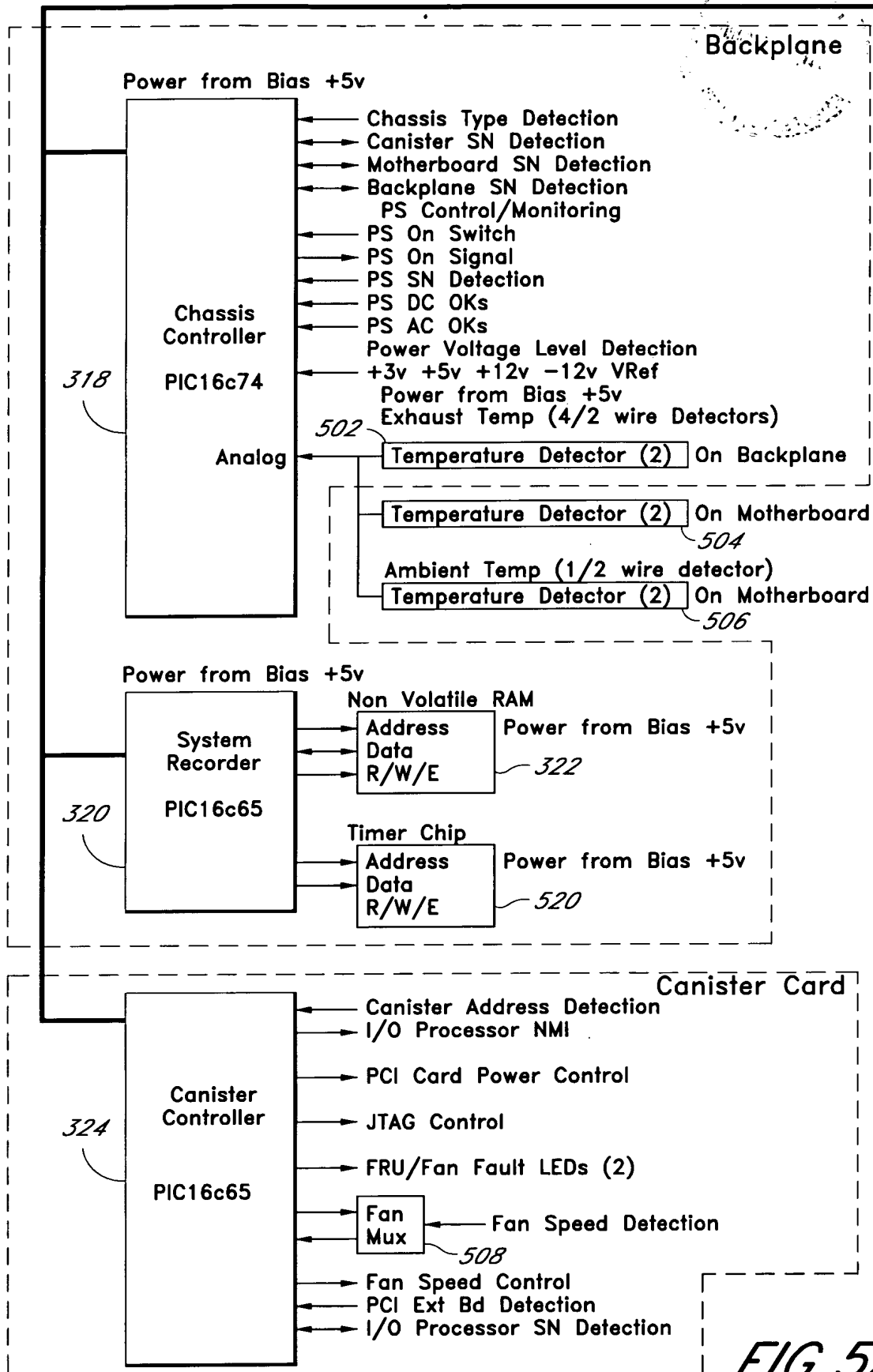


FIG. 5A

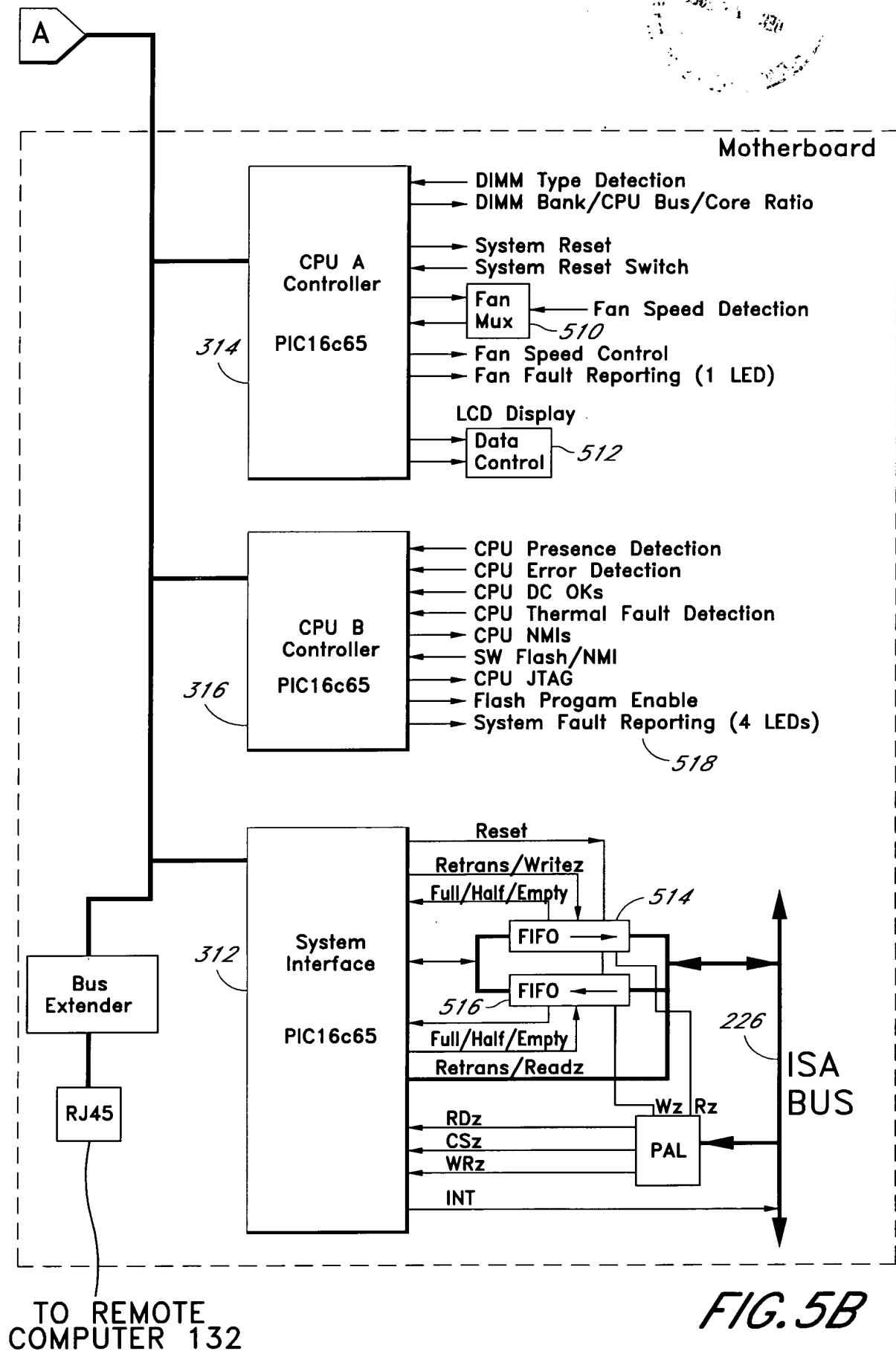


FIG. 5B



332

PROCESS FOR REMOTE CONTROL
OF LOCAL DIAGNOSTIC SERVICES

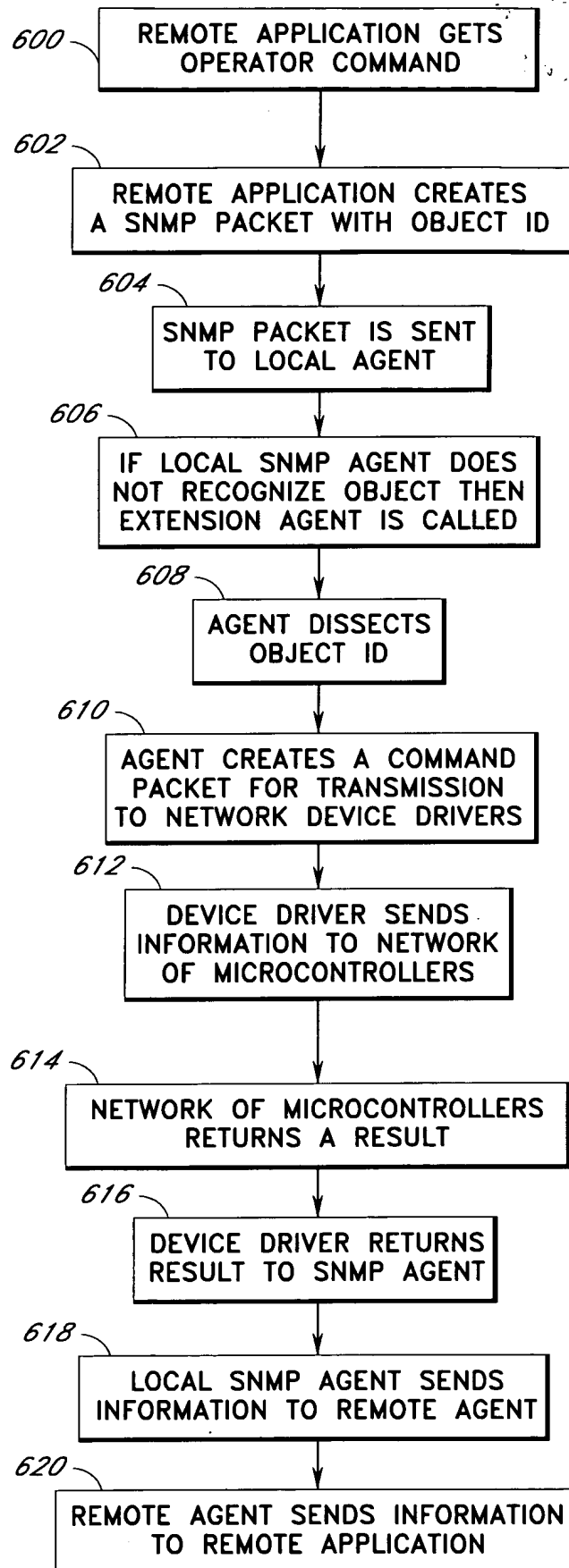


FIG. 6

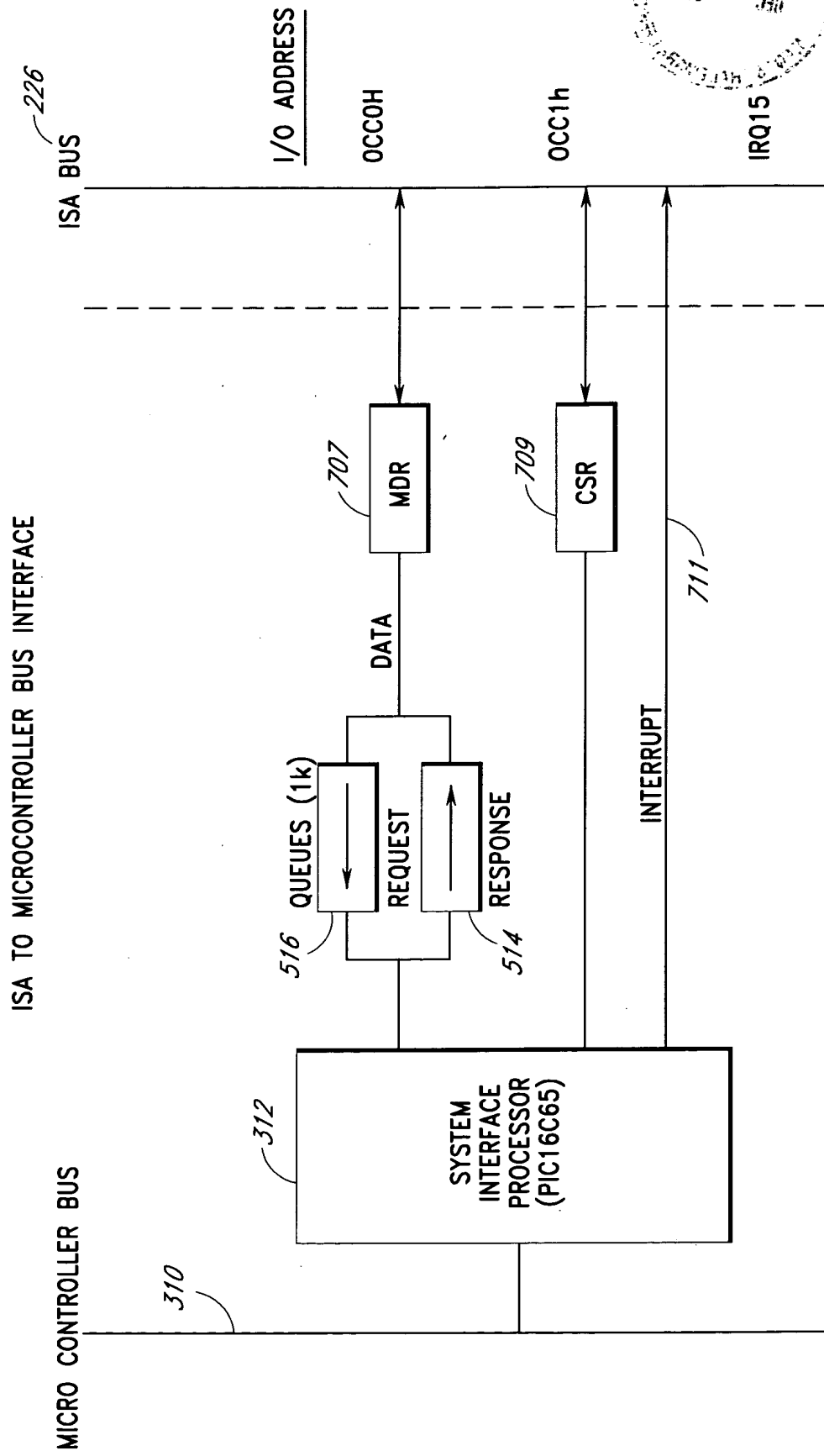


FIG. 7

MASTER TO SLAVE COMMUNICATION

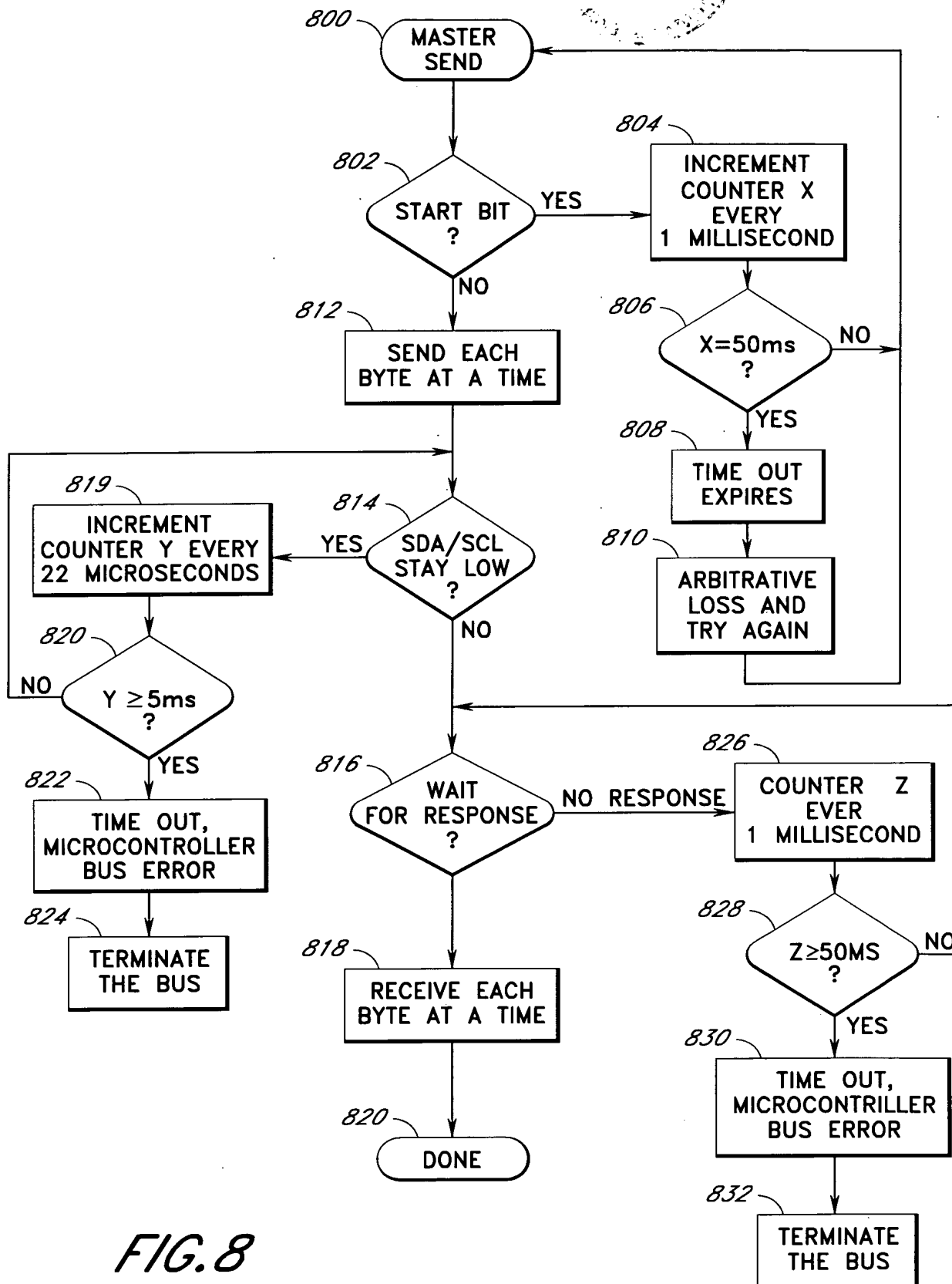


FIG. 8

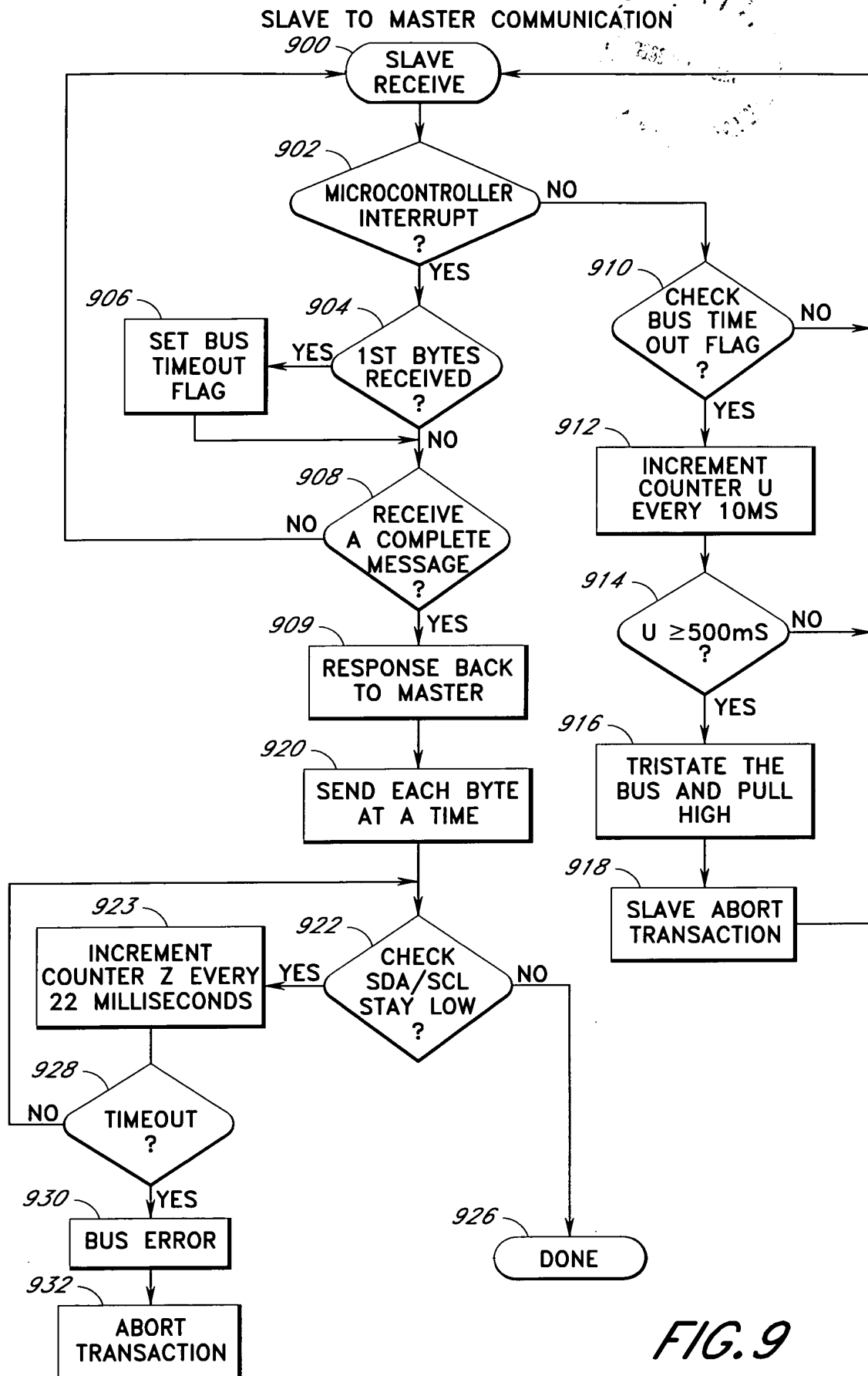
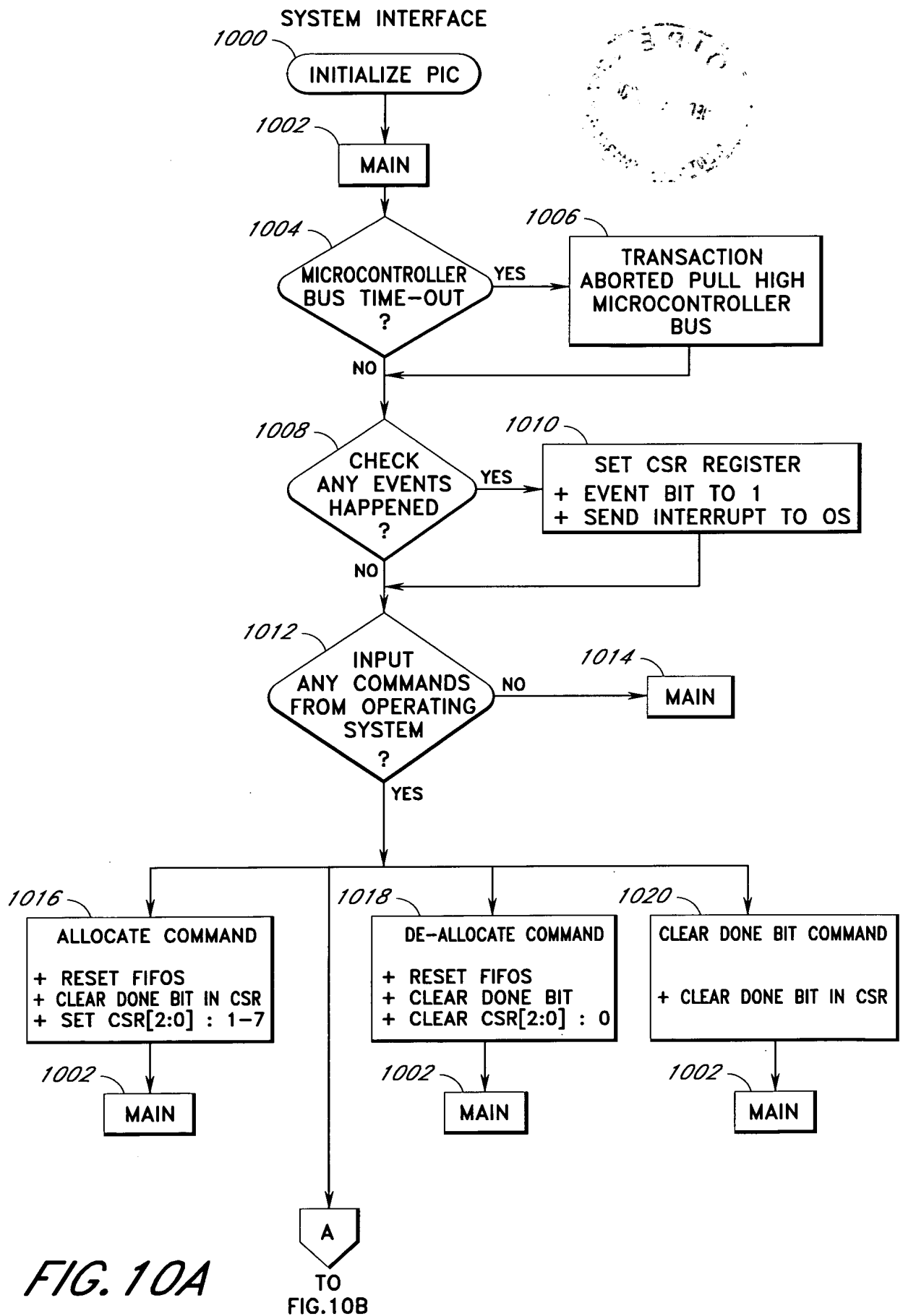


FIG. 9



SYSTEM INTERFACE (CONTINUED)

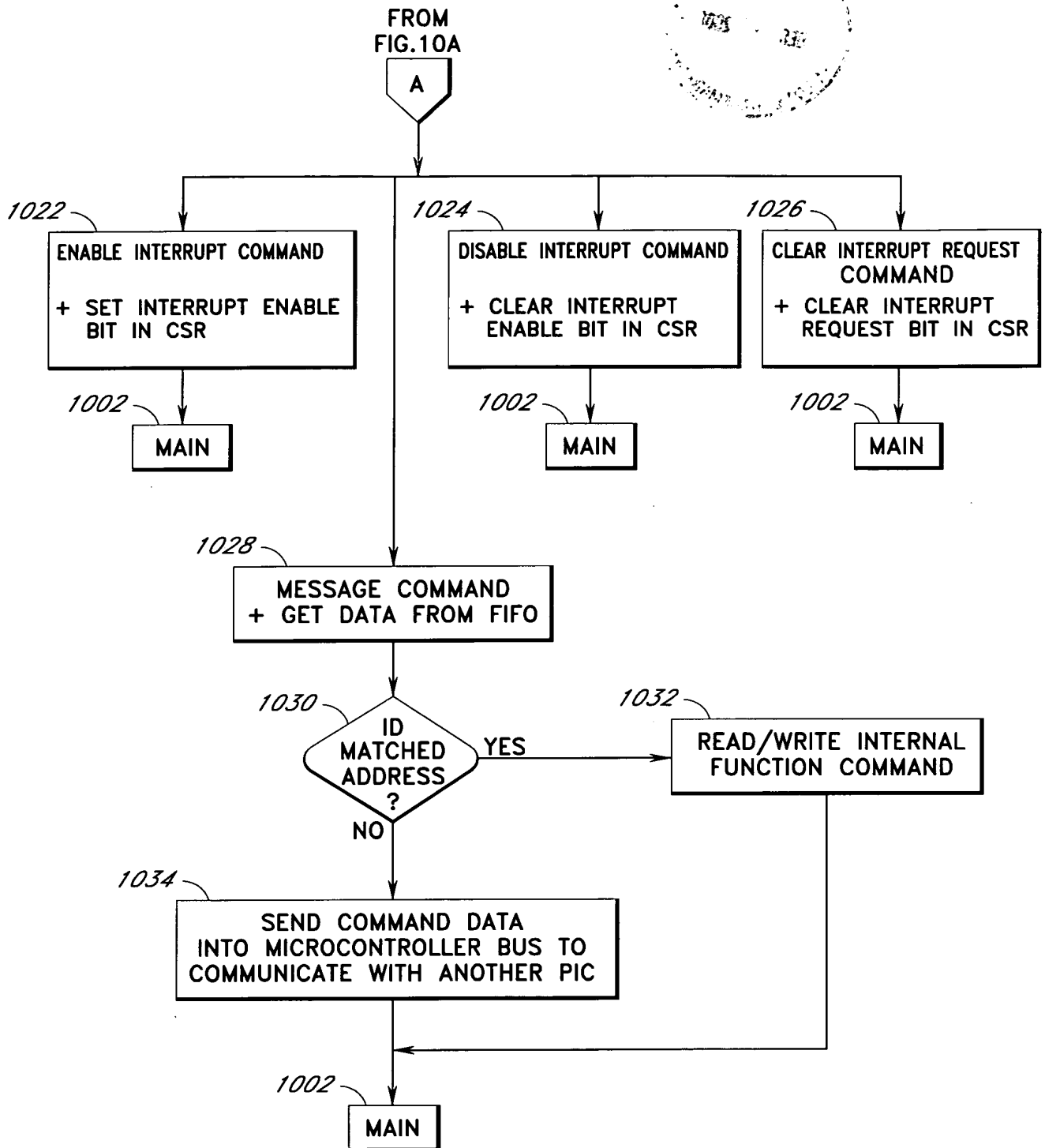


FIG. 10B

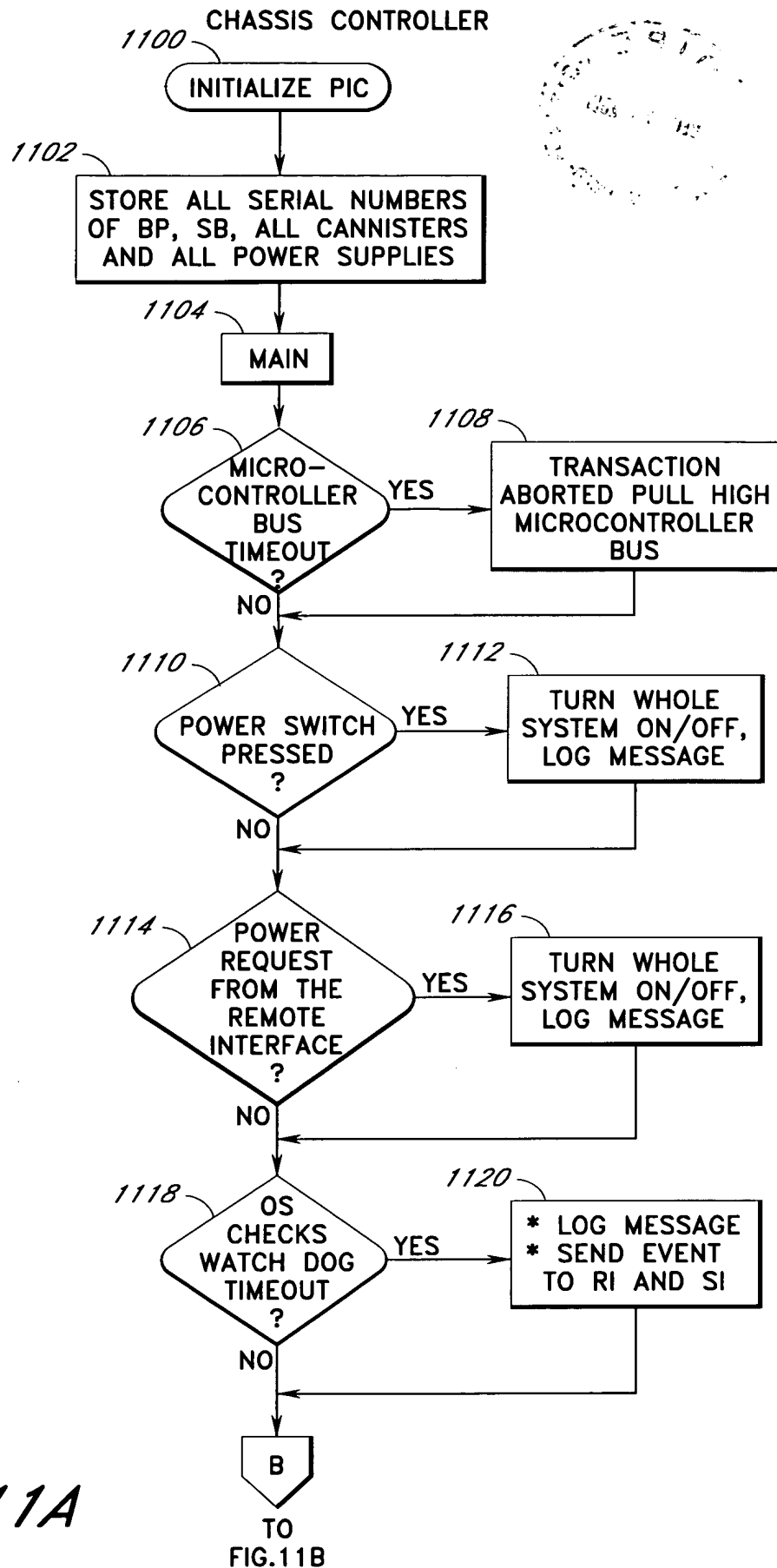


FIG. 11A

CHASSIS CONTROLLER (CONTINUED)

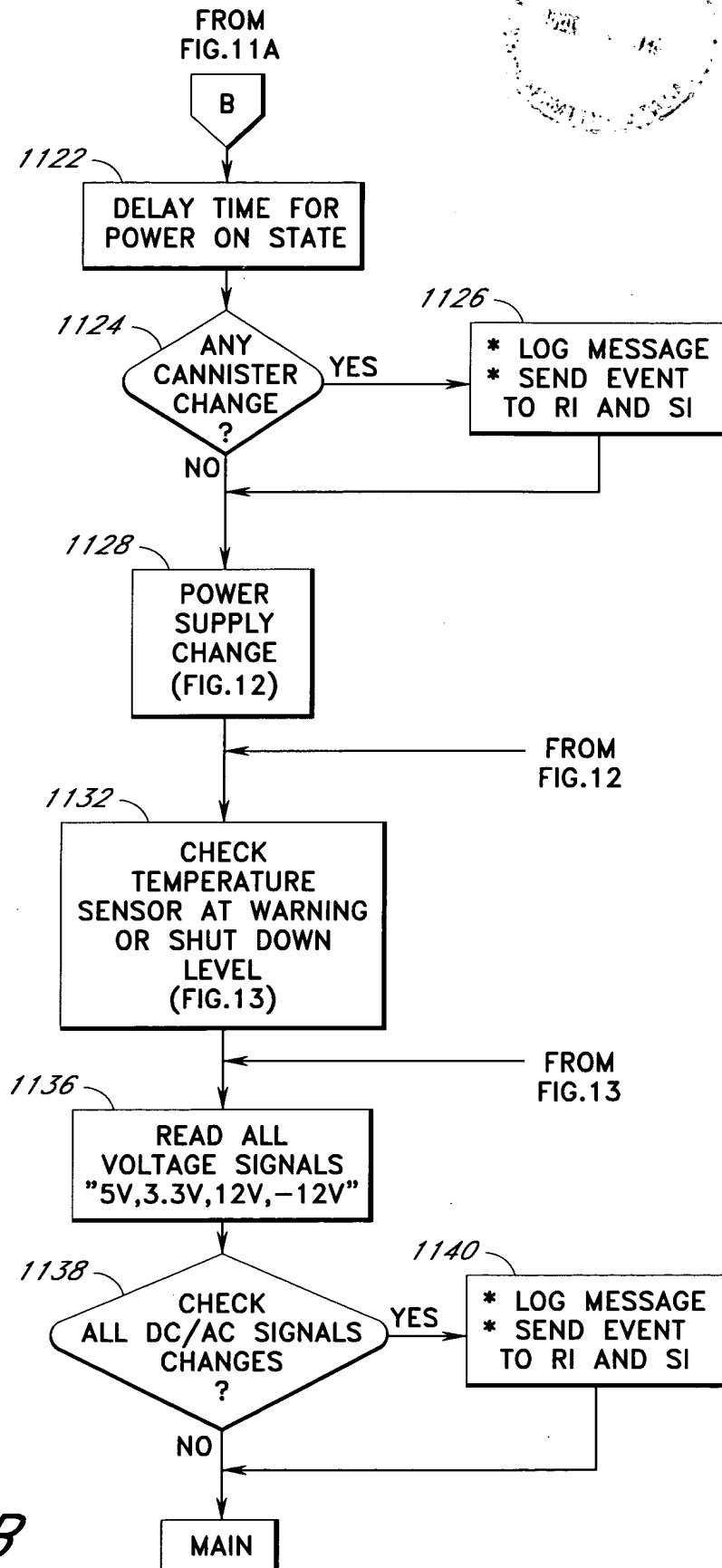


FIG. 11B

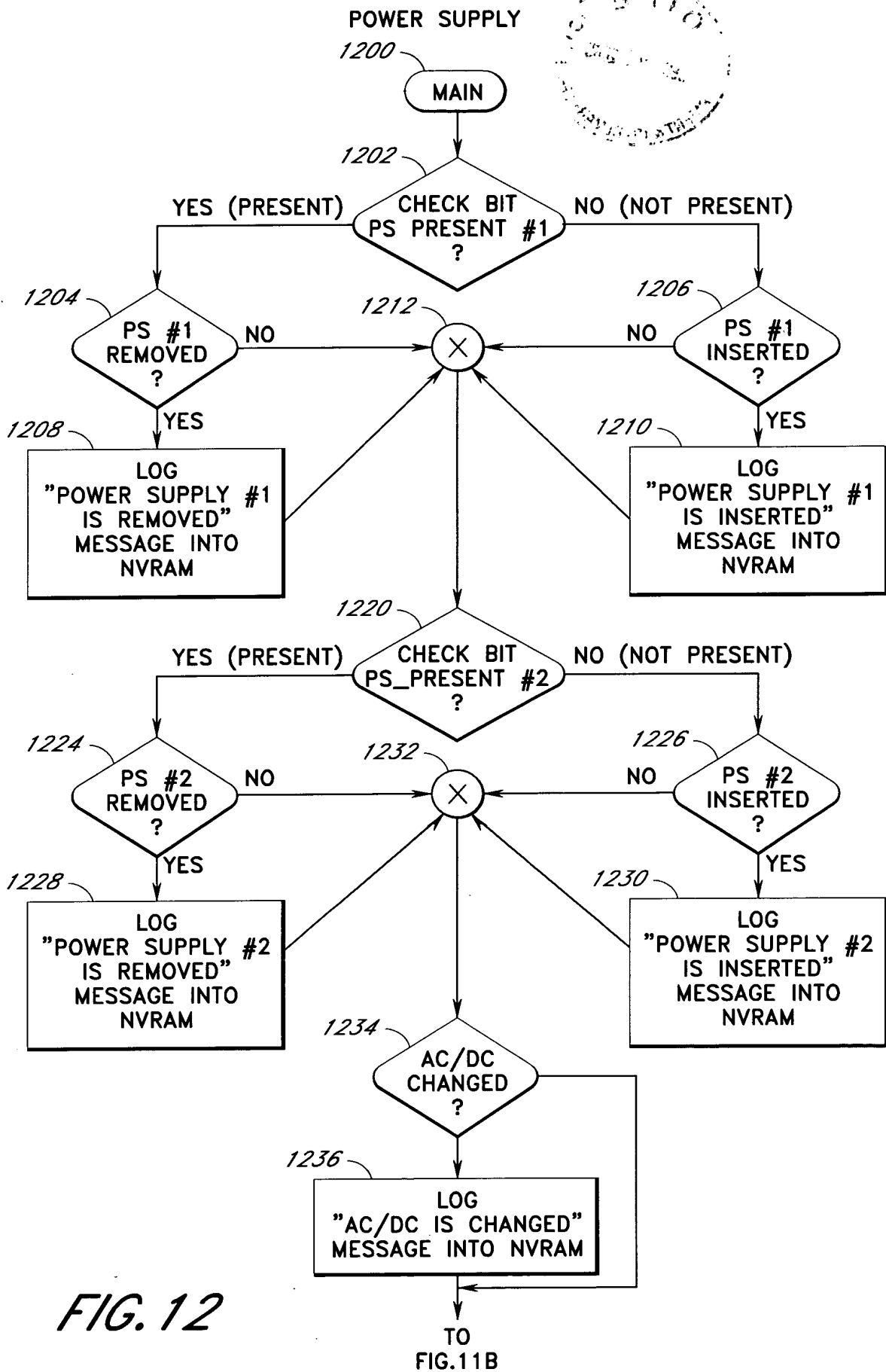


FIG. 12

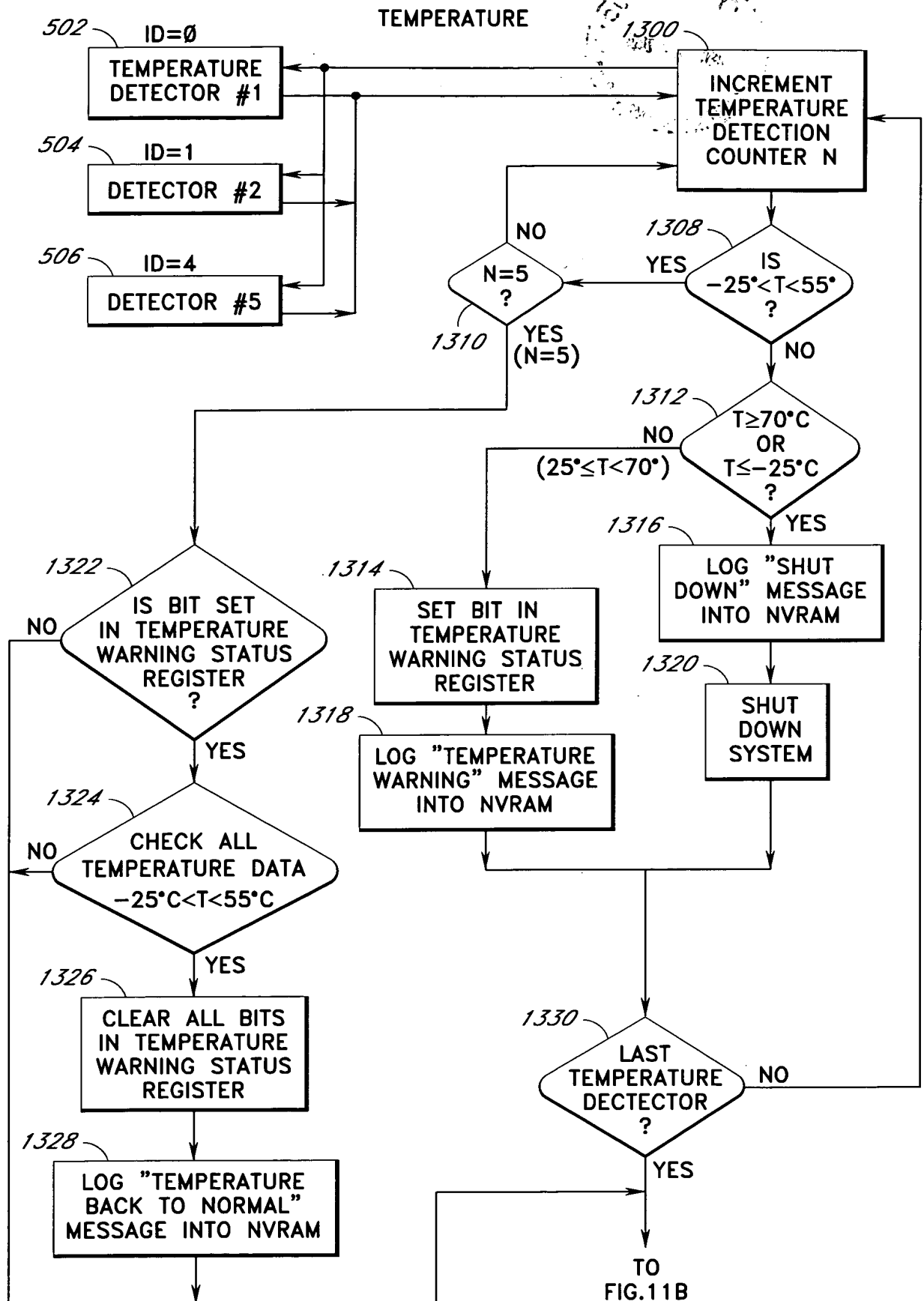


FIG. 13

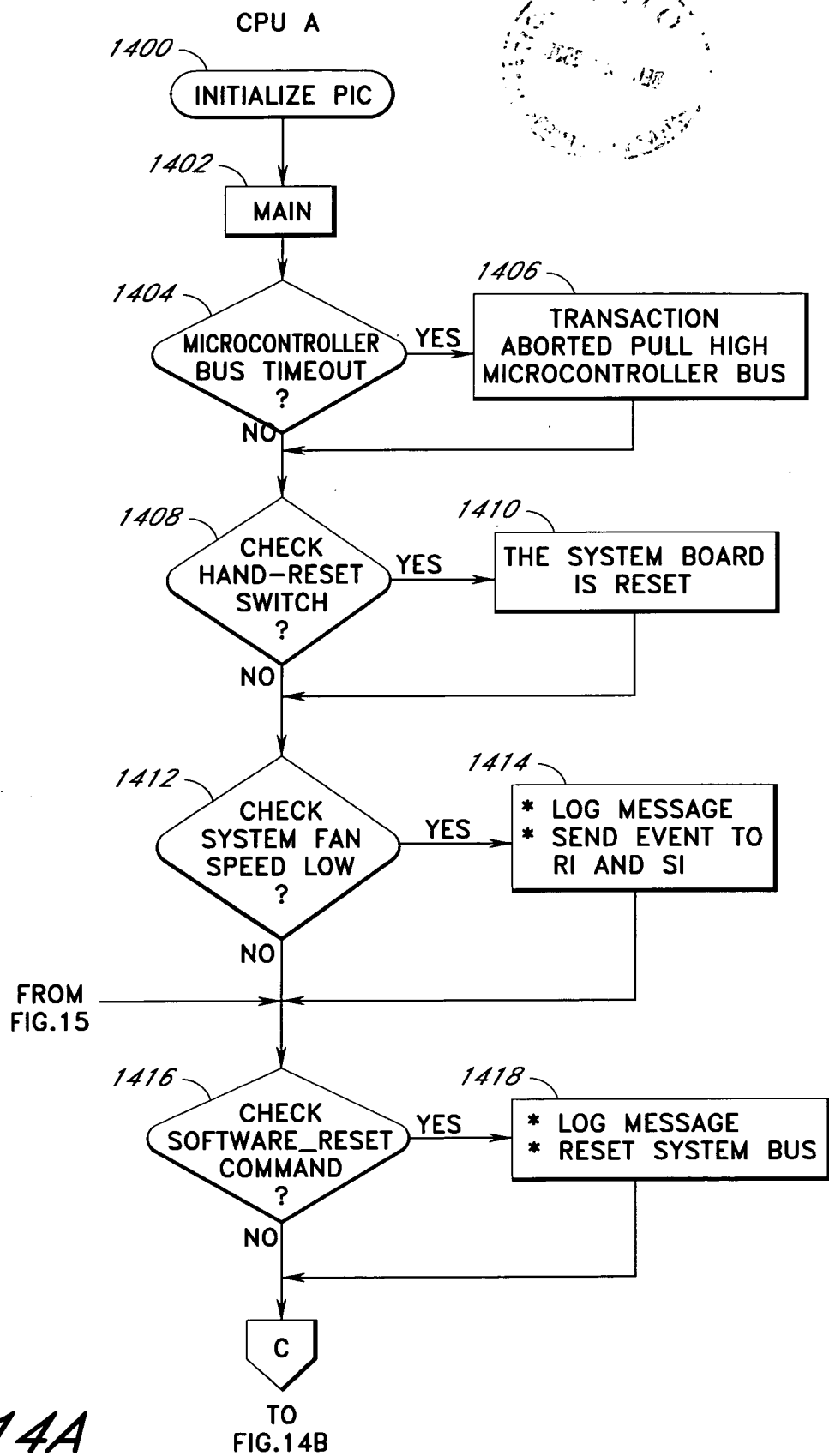


FIG. 14A

CPU A (CONTINUED)

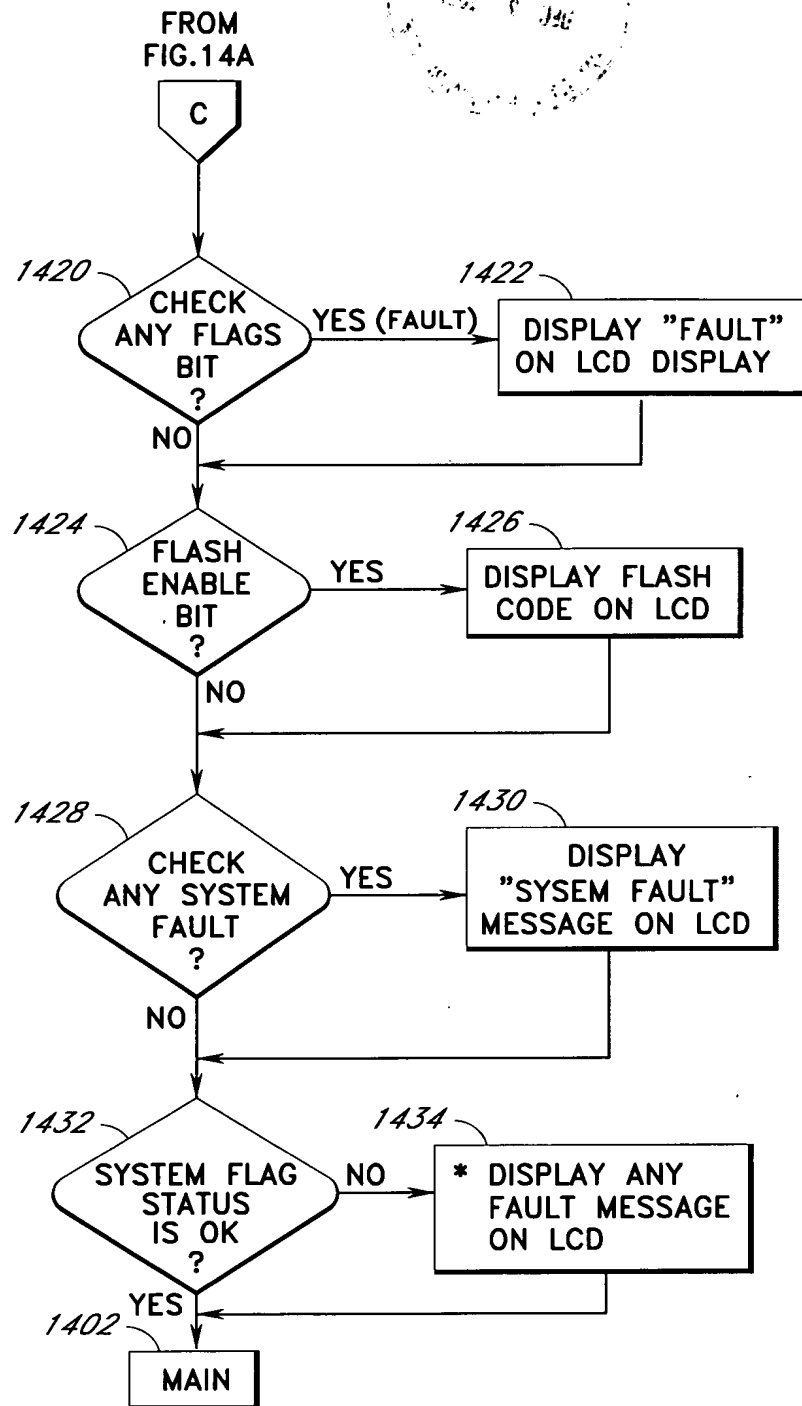


FIG. 14B

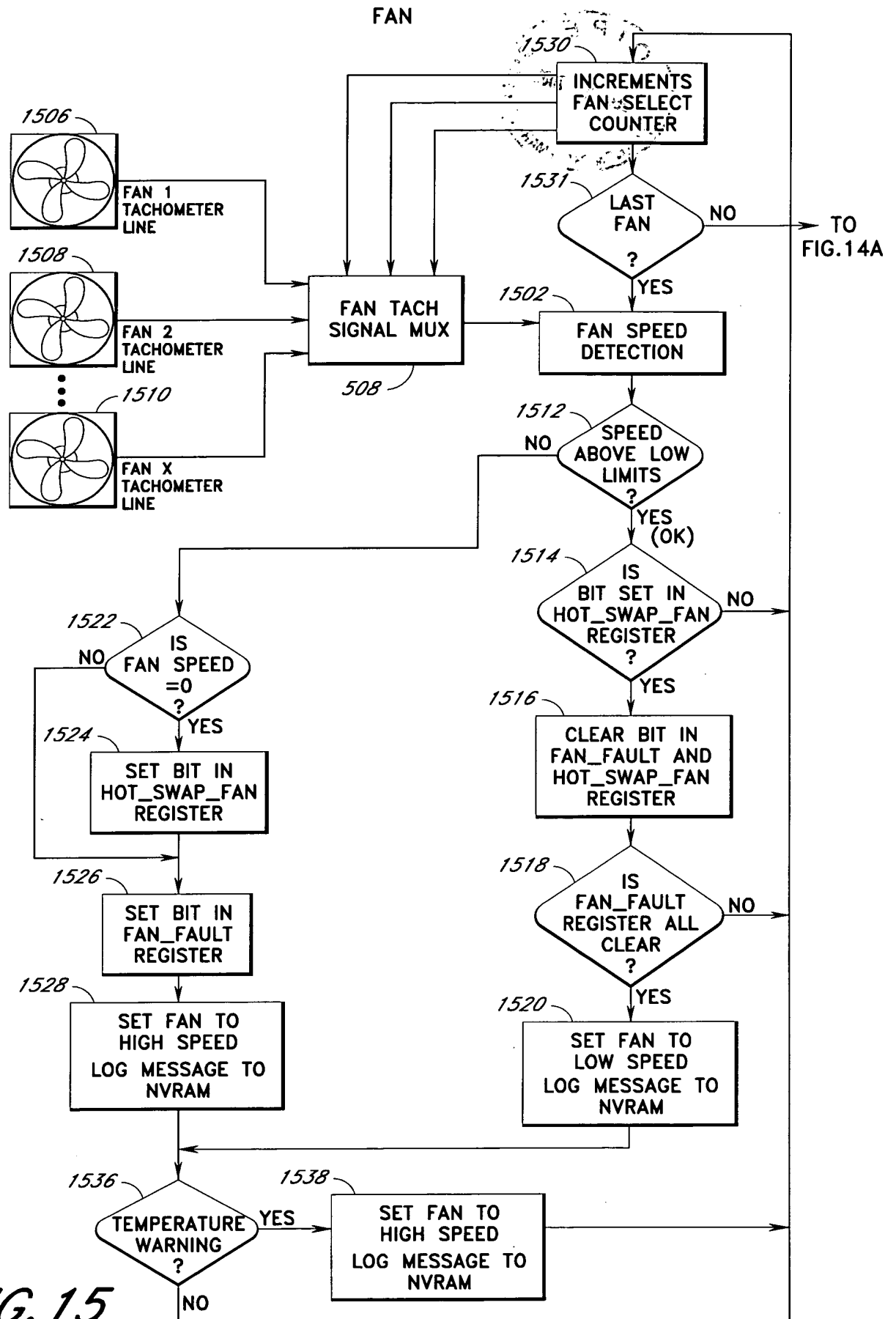
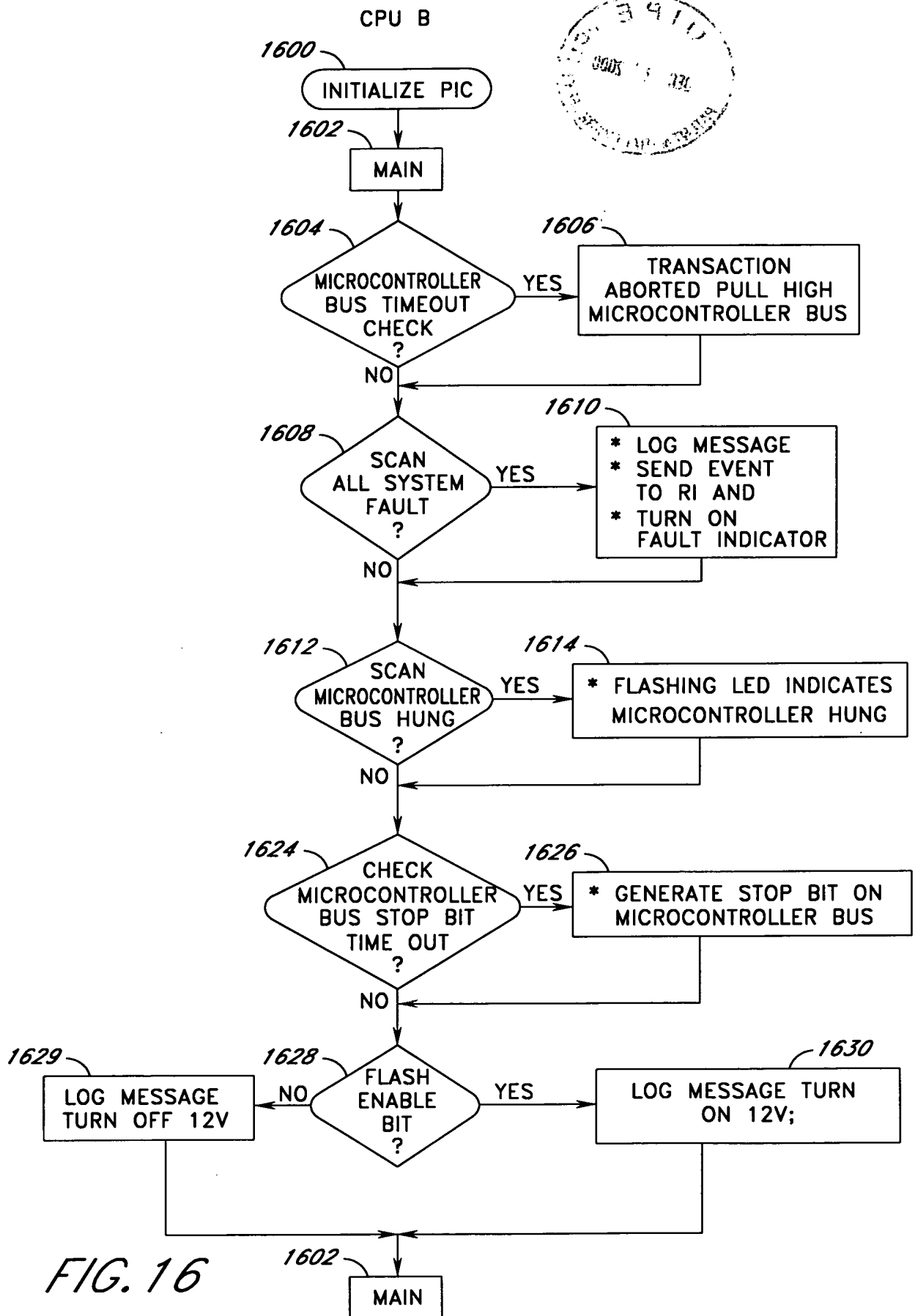


FIG. 15



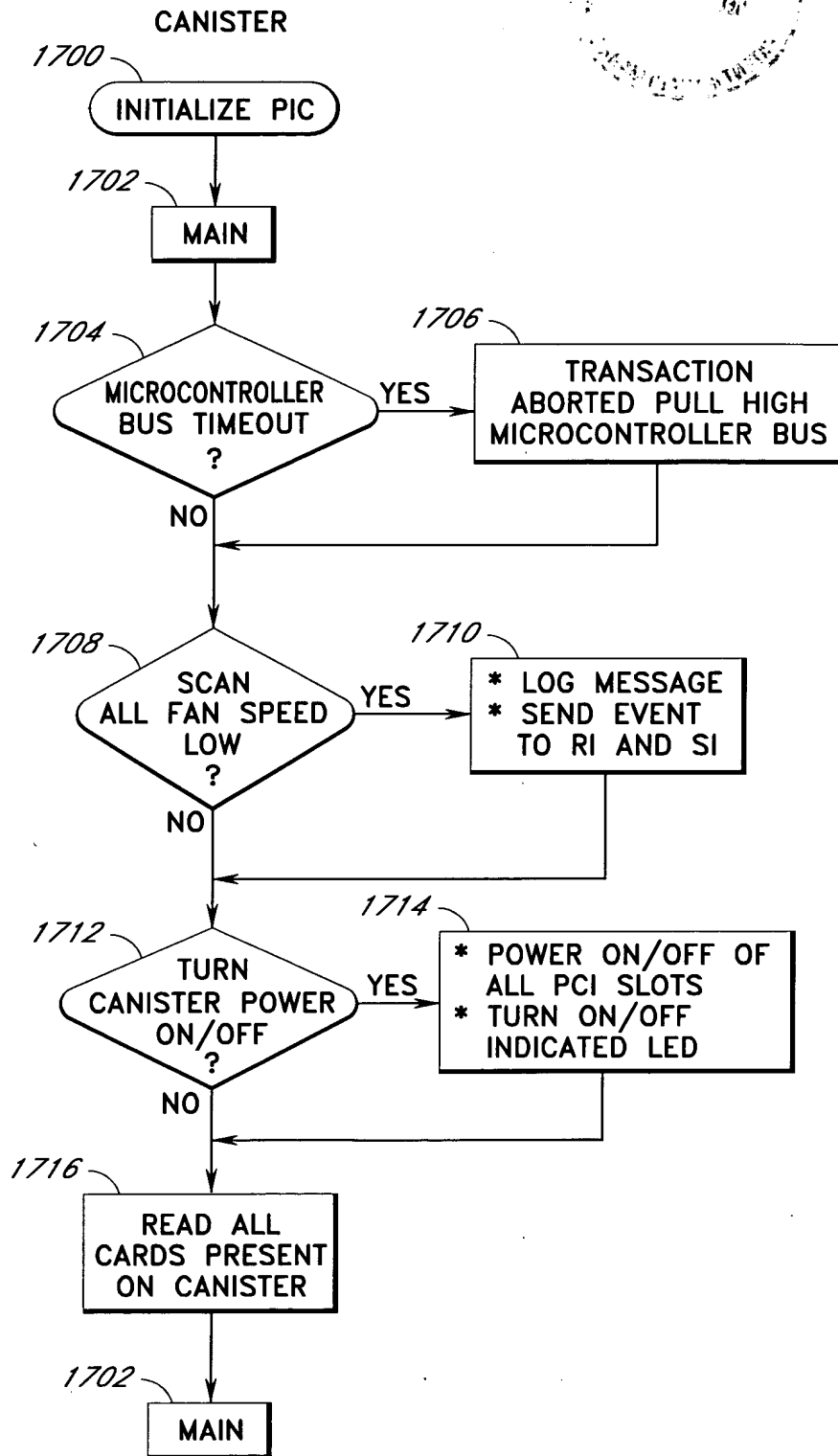


FIG. 17

SYSTEM RECORDER

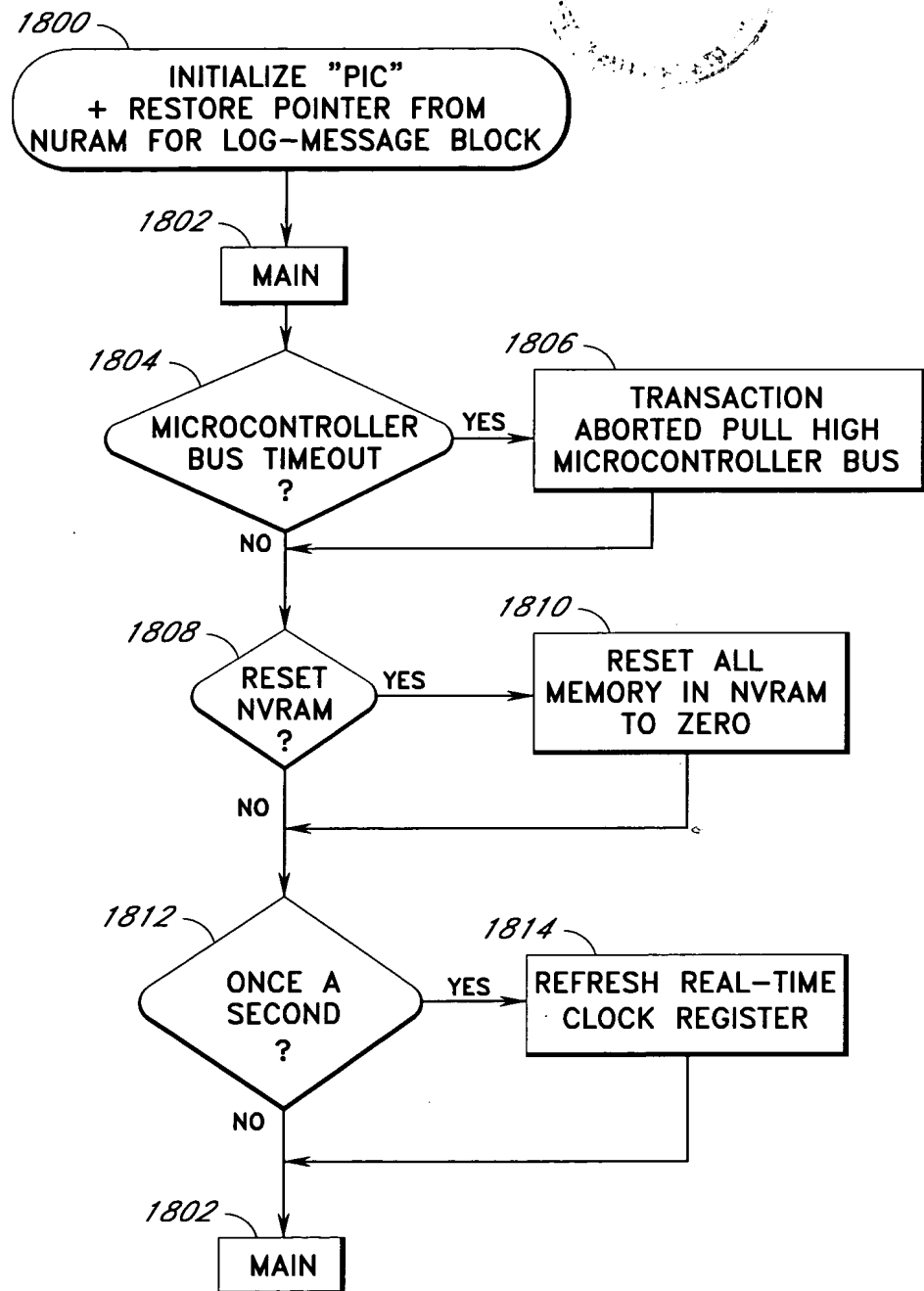


FIG. 18